

Fig. 1

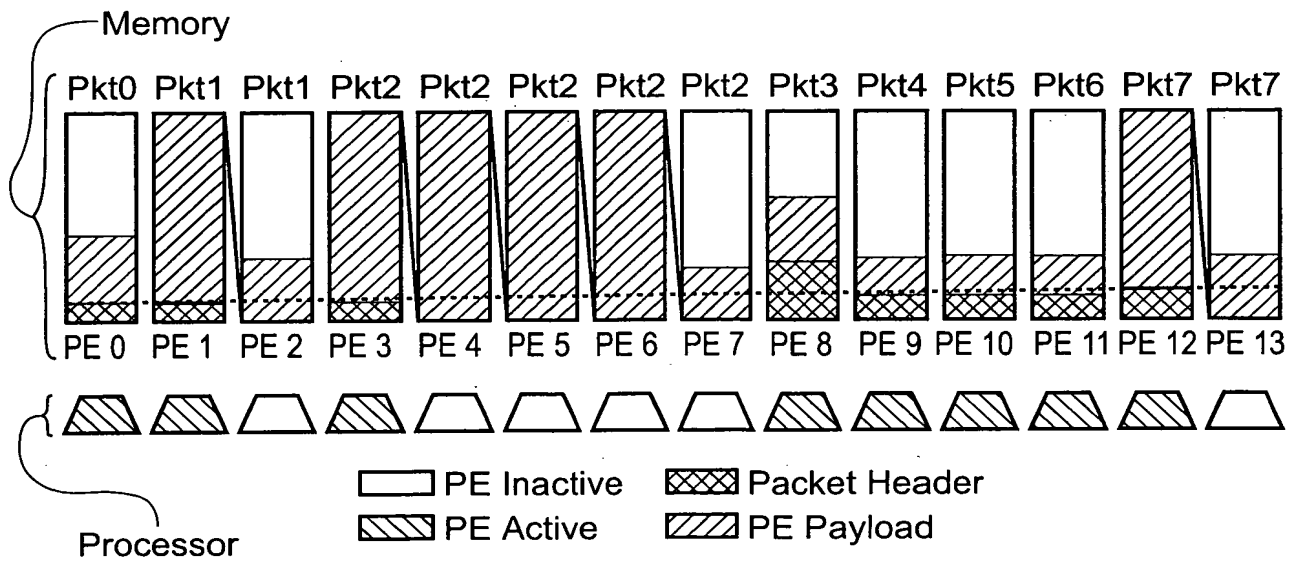


Fig. 2

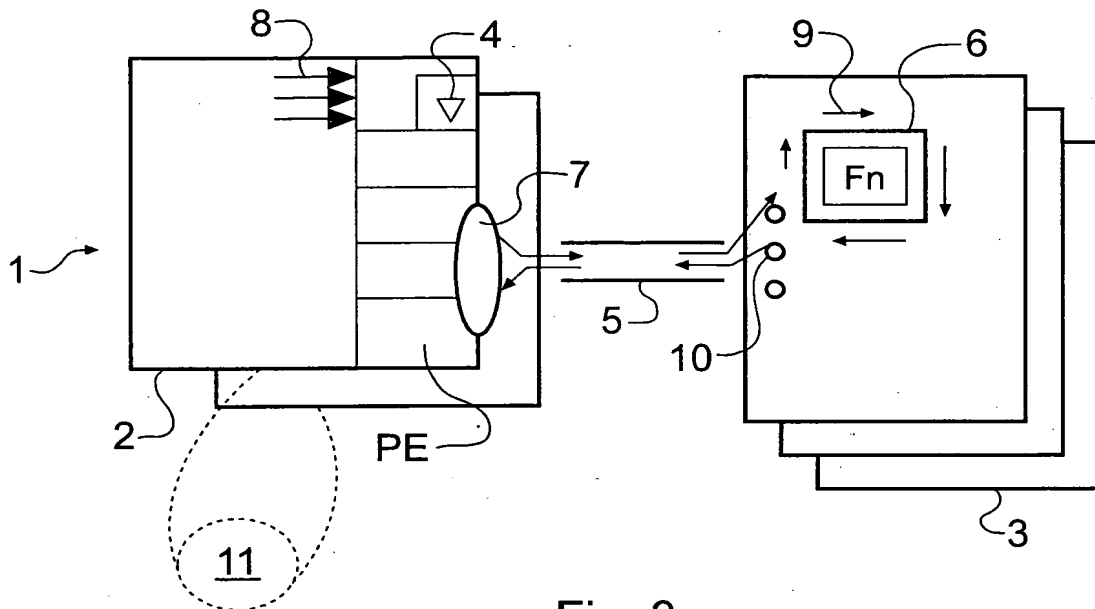
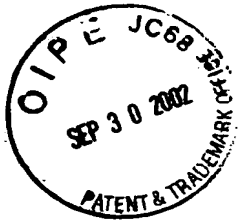


Fig. 3

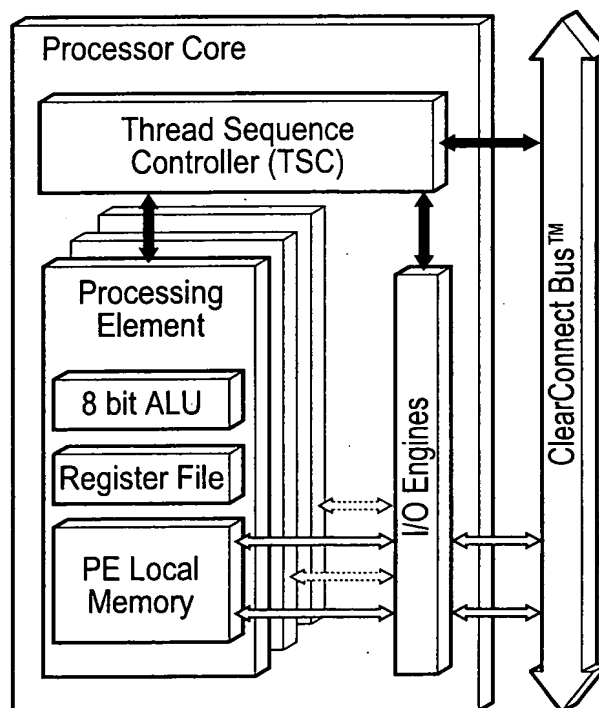


Fig. 4

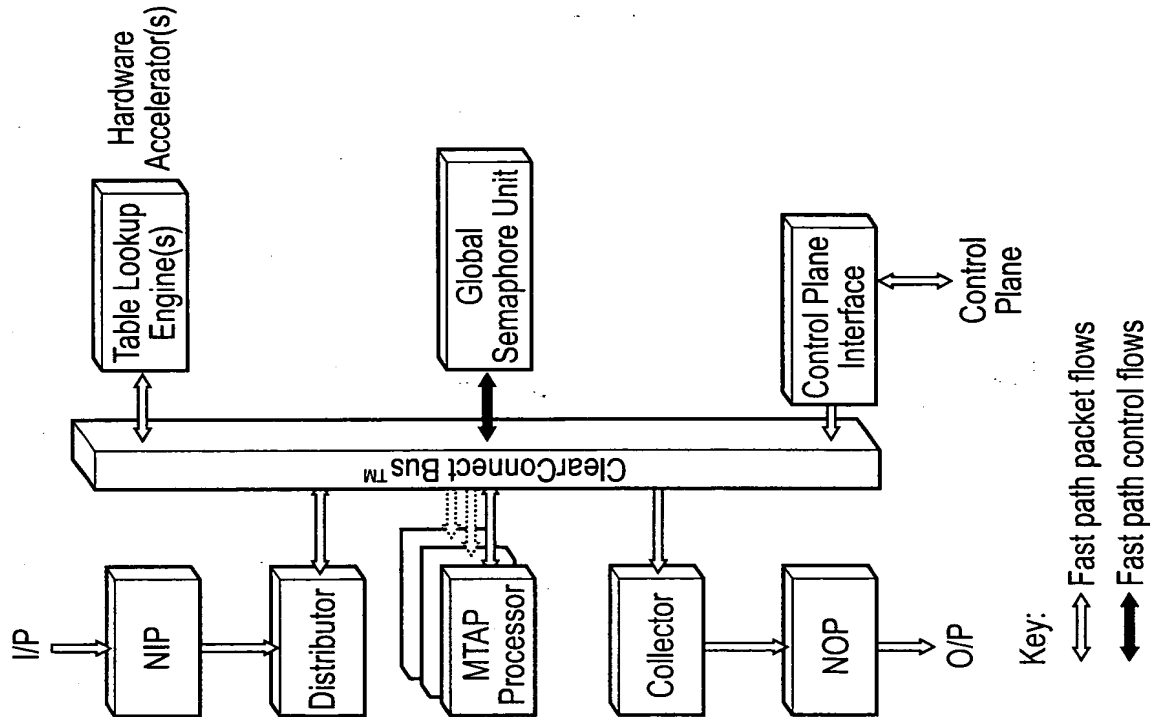


Fig. 6

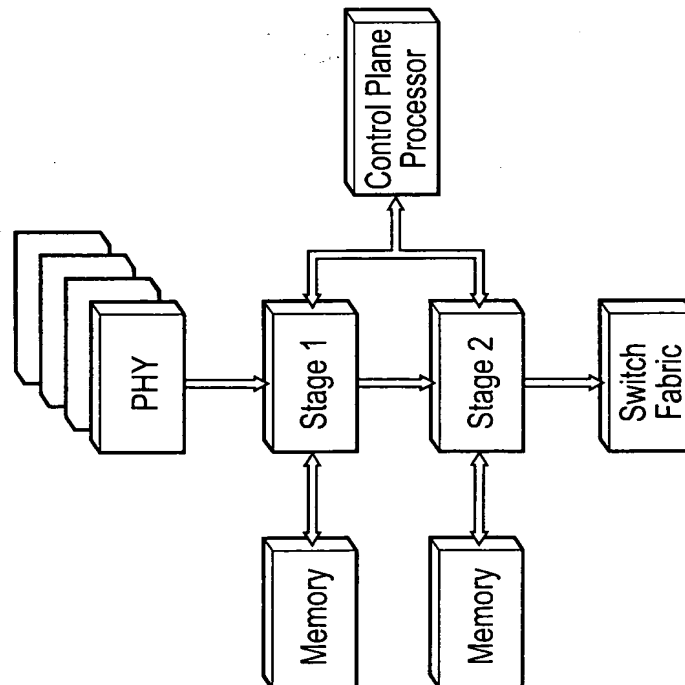


Fig. 5

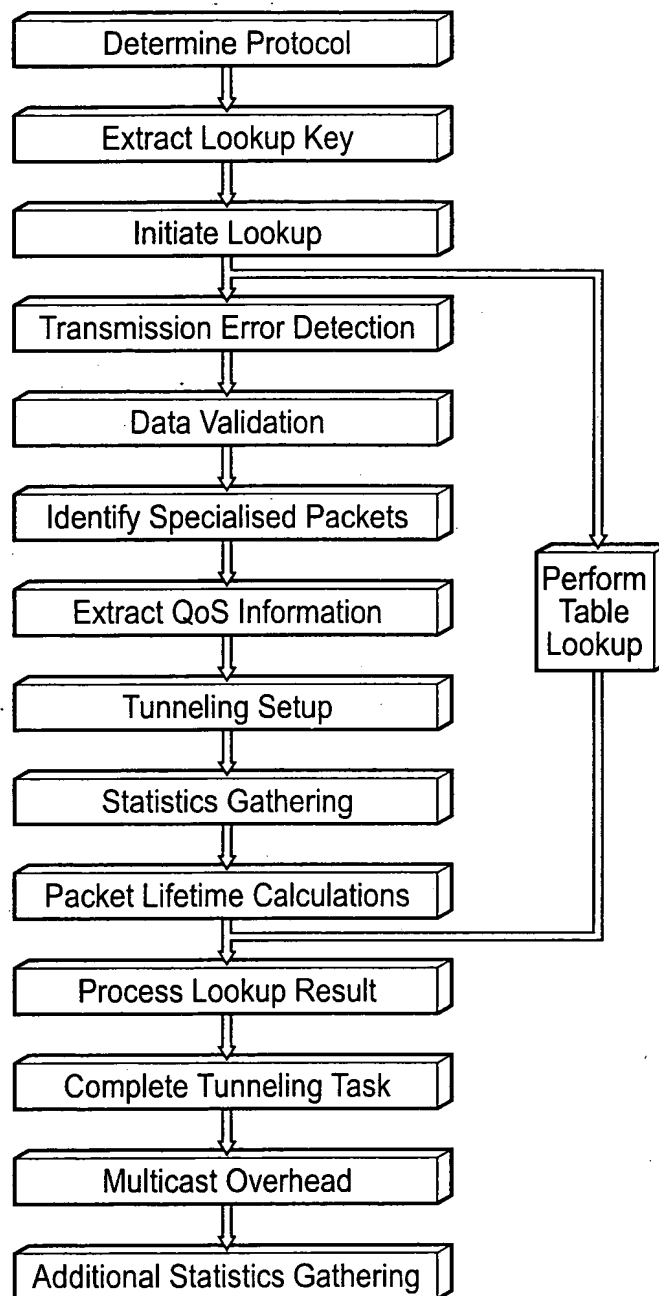
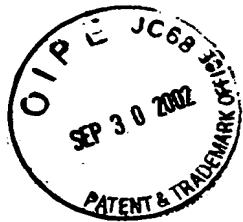
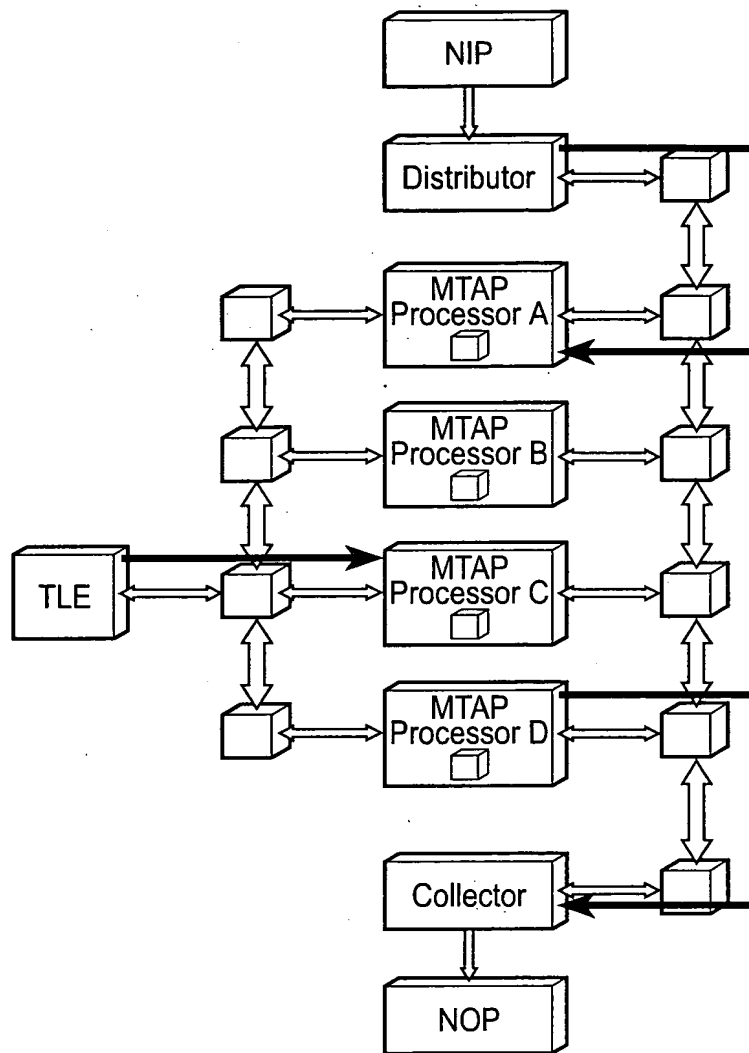


Fig. 8



Key:

↔ Segment of high-speed on-chip network

□ High-speed on-chip network interface

▣ Set of packets being processed

Fig. 9

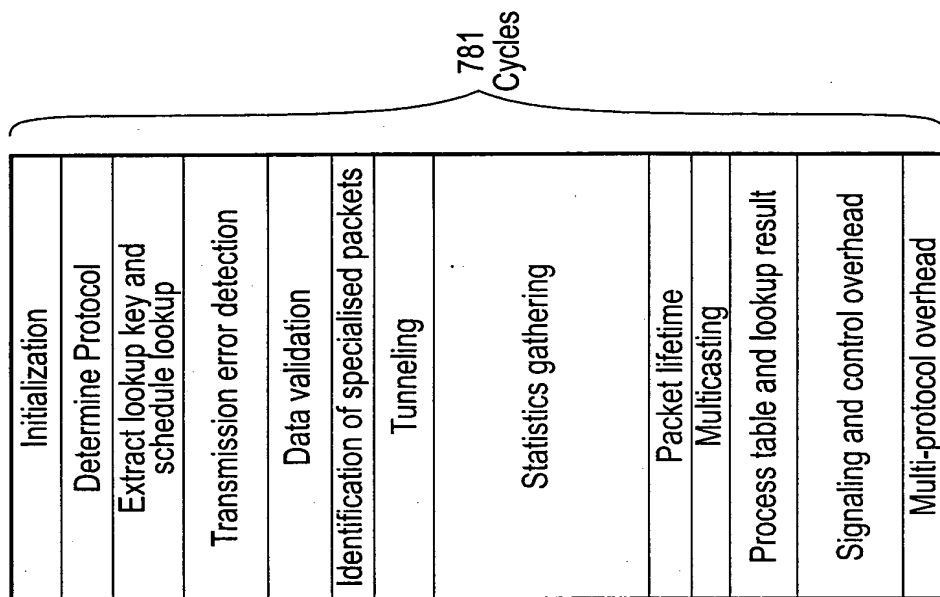
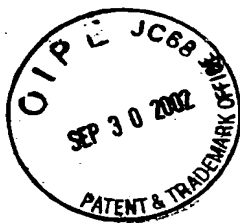


Fig. 11

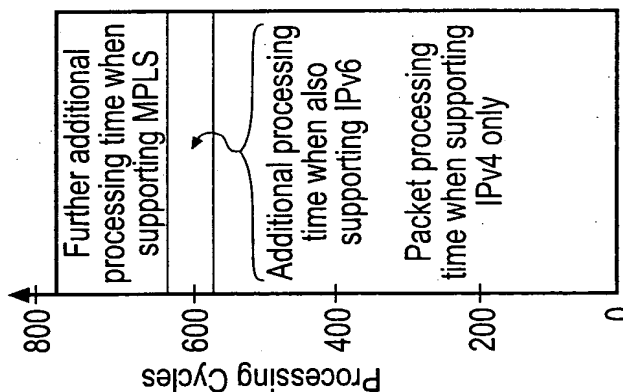


Fig. 10

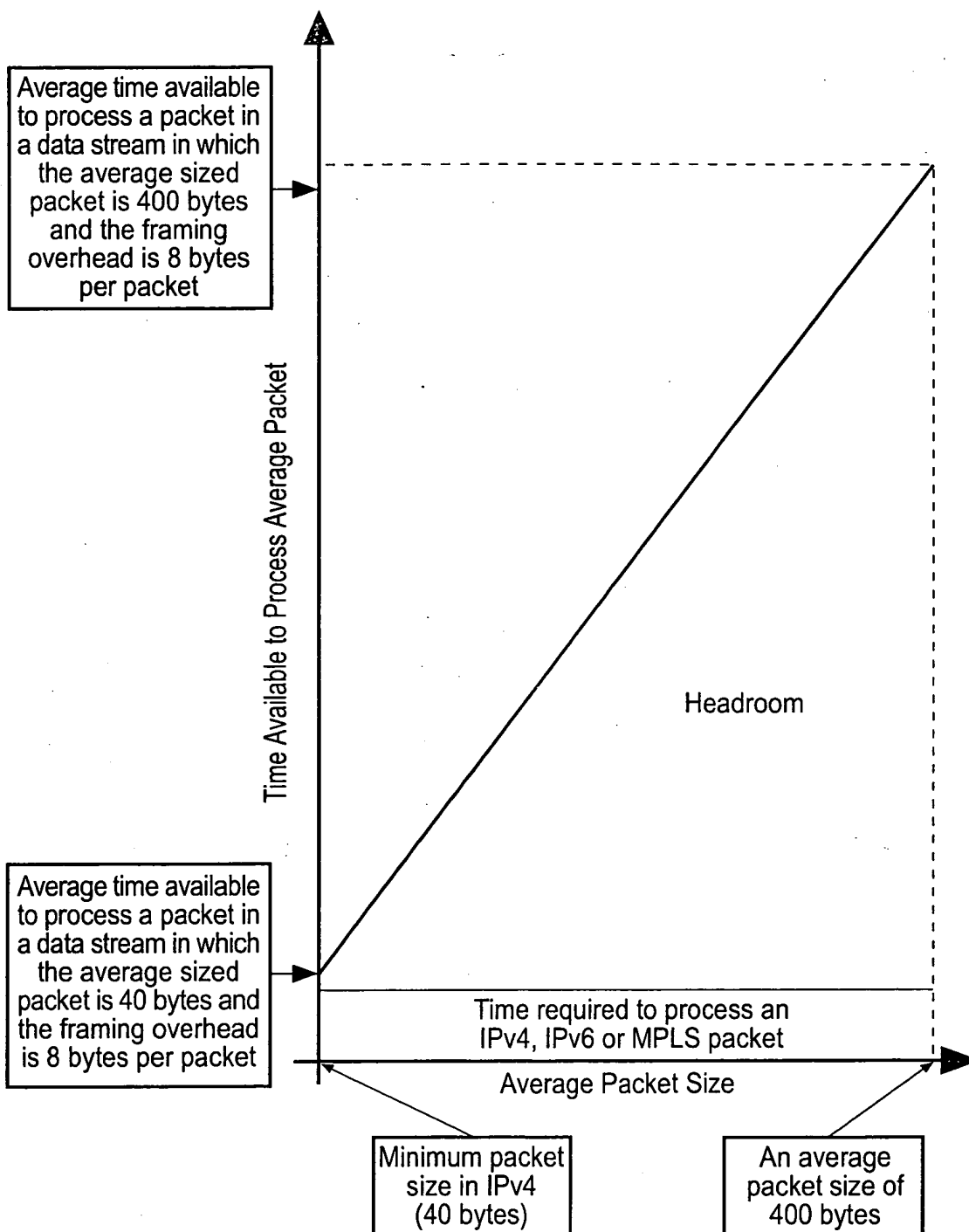
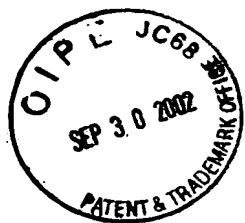


Fig. 12

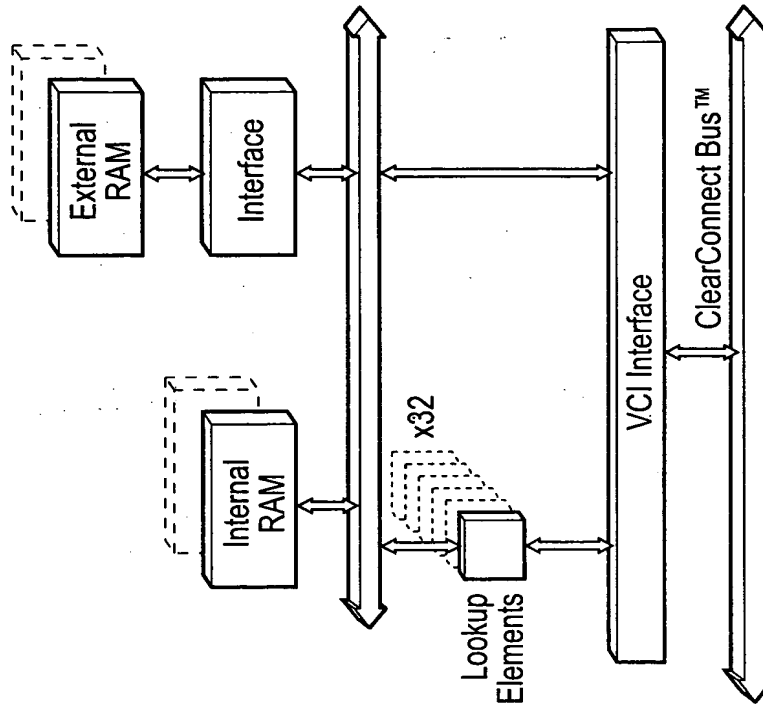
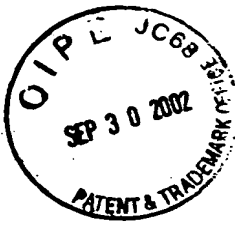


Fig. 14

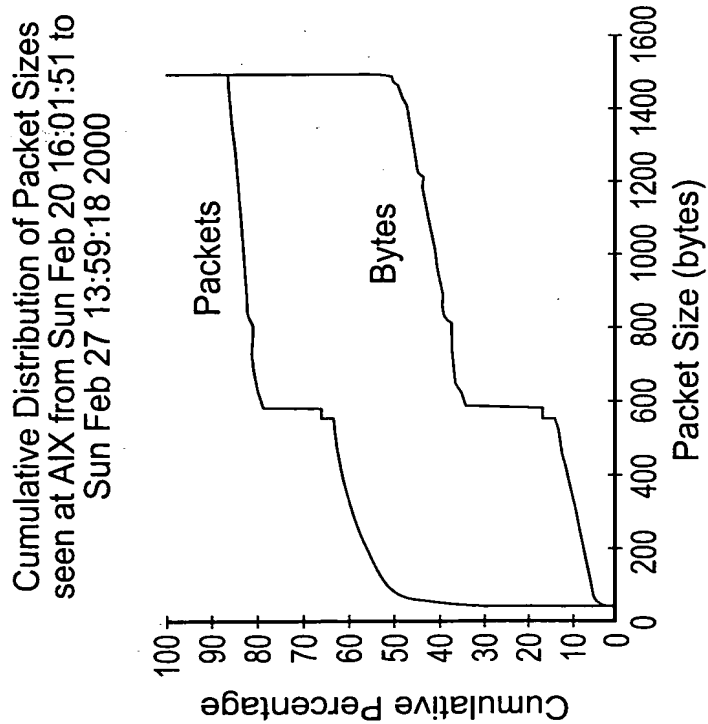


Fig. 7



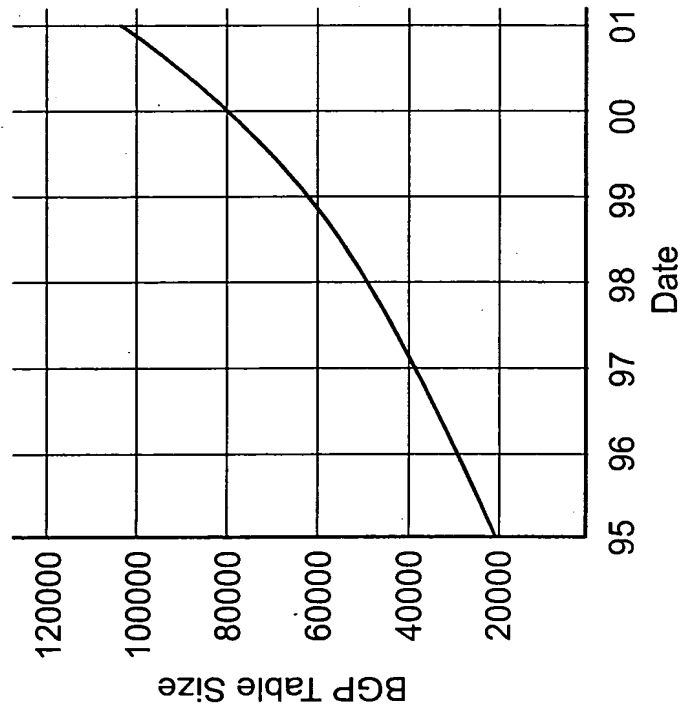
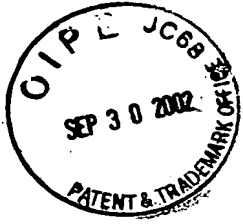


Fig. 13

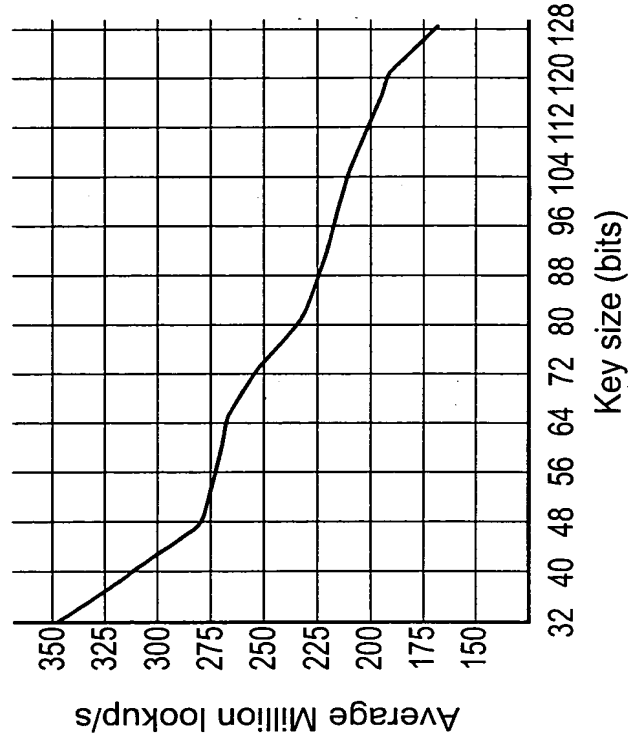


Fig. 15